

## WEST Search History

DATE: Monday, October 29, 2007

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		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L14	l11 and l4	42
<input type="checkbox"/>	L13	l11 and l3	40
<input type="checkbox"/>	L12	l11 and l2	83
<input type="checkbox"/>	L11	l6 or l7 or l10	5447
<input type="checkbox"/>	L10	713/60\$1	2216
<input type="checkbox"/>	L7	(713/50\$1 )[CCLS]	2838
<input type="checkbox"/>	L6	(713/40\$1)![CCLS]	1920
<input type="checkbox"/>	L4	latch\$3 near7 input\$4 near7 output\$4 near7 (equal or "same" or unchang\$4)	1638
<input type="checkbox"/>	L3	(flip\$1flop or flip) near7 input\$4 near7 output\$4 near7 (equal or "same" or unchang\$4)	2187
<input type="checkbox"/>	L2	(first near10 second near10 third) near10 delay near10 clock	1100

END OF SEARCH HISTORY

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10/29/2007  
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## Inventor Name Search Result

Your Search was:

Last Name = NEGISHI

First Name = NOBUJI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08230941</u>	<u>5528241</u>	150	04/21/1994	DIGITAL-TO-ANALOG CONVERTER	NEGISHI, NOBUJI
<u>10540596</u>	Not Issued	121	06/23/2005	Circuit arrangement	NEGISHI, NOBUJI

Inventor Search Completed: No Records to Display.

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<input type="button" value="Search"/>
	<input type="text" value="NEGISHI"/>	<input type="text" value="NOBUJI"/>	

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## Inventor Name Search Result

Your Search was:

Last Name = KISHIDA

First Name = MASAYA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09640733</a>	<a href="#">6888852</a>	150	08/17/2000	SIGNAL COMBINING CIRCUIT HAVING TWO A/D CONVERTERS	KISHIDA, MASAYA
<a href="#">10540596</a>	Not Issued	121	06/23/2005	Circuit arrangement	KISHIDA, MASAYA

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

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<input type="text" value="KISHIDA"/>	<input type="text" value="MASAYA"/>	<input type="button" value="Search"/>

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